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## Smallest Stable Si/SiO<sub>2</sub> Interface that Suppresses Quantum Tunneling from Machine-Learning-Based Global Search

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While the downscaling of size for field effect transistors is highly desirable for computation efficiency, quantum tunneling at the Si/SiO<sub>2</sub> interface becomes the leading concern when approaching the nanometer scale. By developing a machine-learning-based global search method, we now reveal all the likely Si/SiO<sub>2</sub> interface structures from thousands of candidates. Two high Miller index Si(210) and (211) interfaces, being only  $\sim$ 1 nm in periodicity, are found to possess good carrier mobility, low carrier trapping, and low interfacial energy. The results provide the basis for fabricating stepped Si surfaces for next-generation transistors.

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The presence of high-quality Si/SiO<sub>2</sub> interfaces is one of the key reasons that Si acts as the basic material of the semiconductor industry [1]. As required by ever-increasing computation power, the size of microelectronics, in particular, the dimension of field effect transistors (FETs), shrinks continuously and now reaches nanometer regimes. Consequently, the quantum tunneling due to the sharp shortening of the channel length (the distance between electrodes in FET) becomes a leading concern, which can cause severe current leakage and, thereby, high energy costs and heat generation [2]. However, the physical limit for the smallest stable Si/SiO<sub>2</sub> interface [3] remains largely unclear, not to mention their geometry and electronic properties. Fundamentally, this is due to the lack of predicting tools for resolving complex solid-solid interfaces.

The FET, as shown in Fig. 1, consists of a Si channel surrounded by an insulating SiO<sub>2</sub> and gate that connects to electrodes at two ends, referred to the drain and the source. The carrier effective mass  $(m^*)$  of the Si/SiO<sub>2</sub> interface is a key parameter for FETs which controls the current on and off. For the "on" state, a low effective mass is preferable that can lead to high carrier mobility  $(\mu \propto 1/m^*)$  [1] and, thus, large current. In contrast, the "off" state prefers the high effective mass in order to suppress the leakage current. Overall, a trade-off has to be made to achieve both low offstate current and a high on/off current ratio. Nowadays, the  $Si(100)/SiO_2$  interface featuring the low electron effective mass is commonly selected in *n*-type devices, while the  $Si(110)/SiO_2$  interface with the low hole effective mass is utilized in *p*-type devices [3]. To further suppress the quantum tunneling [4], one can either modify the FET architecture, e.g., from the classic planar structure to complex nanofin structure (FinFET) [5] (Fig. 1), or utilize different interface materials, e.g., by replacing Si by MoS<sub>2</sub>. Recently, a 1-nm-gate MoS<sub>2</sub> channel with  $m_e^*$  of  $0.55m_e$  was reported, which shows a high on/off current ratio of  $\sim 10^6$  and more than 2 orders of magnitude reduction in off-state leakage currents relative to Si (cf.  $m_e^* \sim 0.19m_e$  for Si [100]) [6]. Nevertheless, Si remains the dominant semiconducting material, and how to regulate the effective mass of Si by designing optimal Si/SiO<sub>2</sub> interfaces is a top challenge in the field.

In the FinFET architecture, new Si/SiO<sub>2</sub> interfaces, particularly those with high-index facets, emerge at the fin's sidewalls and tips. In manufacturing ever-smaller-sized FinFET, it becomes imperative to design these Si/SiO<sub>2</sub> interfaces to achieve high performance. The search for the optimal Si/SiO<sub>2</sub> interface is, however, a formidable task in theory. First, the existing simulation methods cannot predict the orientations of Si/SiO<sub>2</sub> interfaces. In fact, most previous calculations were carried out on low-index Si(100)/SiO<sub>2</sub>, simply because it is the known facet in classic



FIG. 1. Structures of FETs. (a) Classic planar FET; (b) modern FinFET. *L* represents the channel length.



FIG. 2. (a) The flowchart of the ML-interface method. (b) Atomic structures of ten  $Si/SiO_2$  interfaces with an interfacial area less than 1 nm<sup>2</sup> in periodicity. The dashed cycles in (b) highlight the unsatisfied Si atoms with dangling bonds. Yellow balls, Si; red balls, O.

planar FETs. Second, Si facets may well undergo reconstruction due to the presence of dangling bonds, leading to the great complexity of surface structures. Third, the heterogeneous solid junction for Si/SiO<sub>2</sub> may well not obey the stoichiometry of chemical valence, where the Si:O may vary to reach the highest stability. In fact, bulk SiO<sub>2</sub> could be amorphous, which further complicates the interface formed in between Si and SiO<sub>2</sub>, although the crystalline interface was suggested in experiments [7]. For these reasons, it remains largely unknown to date for the geometry and electronic structures of the stable Si/SiO<sub>2</sub> interfaces.

Herein, we develop a machine-learning-assisted phenomenological theory for the global search of interface structure between solids (ML interface). The method is utilized to predict the Si/SiO<sub>2</sub> interfaces by considering all the likely surface reconstructions and composition variation. Ten stable Si/SiO<sub>2</sub> interfaces with small interfacial area in periodicity are thus identified by density functional theory (DFT) with PBE (Perdew-Burke-Ernzerhof) exchange-correlation functional and the associated properties of the interfaces, including the interfacial-danglingbond contents, band structure, and carrier effective mass at various Si orientations are derived from hybrid HSE06 (Heyd–Scuseria–Ernzerhof) functional.

Our ML-interface method to determine the interface structure is schematically illustrated in Fig. 2(a), which has three steps: (i) orientation relationship (OR) screening, (ii) atomic structure of interface generation from graph theory, and (iii) global optimization based on global neural network potential. Below, we briefly introduce each step, and the details are elaborated in Supplemental Material [8–10].

$$\mathbf{F} = (\mathbf{A}\mathbf{T})^{-1}\mathbf{B}\mathbf{M} = \mathbf{R}\mathbf{U}.$$
 (1)

The first step, OR screening, aims to identify the lattice correspondence matrix F between two different solid phases, as shown in Eq. (1), where **T** and **M** are the primitive lattice parameters of two solid phases, respectively; A and B are the transformation matrices that need to search. The F matrix can also be decomposed into two parts: the rotation matrix  $\mathbf{R}$  and the homogeneous stretch matrix U. All these matrices are  $(3 \times 3)$ . By enumerating possible A and B matrices, we can obtain a series of U, which can lead to the OR for the interface with the best lattice match according to the phenomenological theory of martensite crystallography (PTMC). We note that PTMC was originally developed to explain the martensitic transformation (e.g., between  $\gamma$ -Fe and  $\alpha$ -Fe) by merely using the lattices of contacting phases, and it totally neglects the atomic matchness at the interface [11]. In step 2, a graphbased approach utilizes the OR obtained from step 1 to build the atomic structure of interface by gluing two surface slabs, where the max-flow min-cut algorithm from graph theory is applied to maximally retain the bonding at the interface [12]. Finally, also the most important step, our recently developed SSW-NN method; i.e., stochastic surface walking (SSW) globally optimized based on the global neural network potential [13] is utilized to extensively search for the global minimum of the interface structure built from step 2 (see Supplemental Material, Sec. 1.4 [8], for details on building the global neural network potential). As illustrated in Eq. (2), the key of the SSW method for a global PES search is the modification of PES  $V_{mod}$  by continuously adding Gaussian-type bias potential  $v_n$  along

TABLE I. The parameters for ten stable  $Si/SiO_2$  interfaces with an interfacial area less than 1 nm<sup>2</sup>.

OR	a/a' (Å)	b/b' (Å)	heta/ heta' (°)	$\gamma (J/m^2)$	$m_h^* (m_e)^{\mathrm{a}}$	$m_e^* (m_e)^a$
$(100)_{si}    (111)_{\alpha-crist}$	7.72/7.03	8.64/8.49	63/65	0.93	8.58	0.23
$(111)_{\rm Si}    (110)_{\alpha-{\rm crist}}$	6.69/6.88	7.72/7.03	90/90	1.13	0.38	0.94
$(110)_{\rm Si}    (100)_{\alpha-\rm quartz}$	5.46/5.46	15.45/14.94	90/90	1.27	0.32	16.89
$(531)_{\mathrm{Si}} \parallel (010)_{\beta - \mathrm{tridymite}}$	8.64/8.61	10.22/10.55	90/90	1.01	1.14	4.90
$(210)_{\mathrm{Si}} \parallel (102)_{\beta - \mathrm{tridymite}}$	5.46/5.27	12.21/12.56	90/90	1.16	0.48	6.76
$(211)_{\rm Si} \  (1\bar{1}2)_{\alpha-{\rm crist}} \ $	9.46/9.84	7.72/7.03	90/90	1.17	6.53	0.63
$(511)_{\rm Si} \parallel (21\overline{1})_{\alpha-{\rm quartz}}$	10.22/10.20	7.72/7.39	79/84	1.20	0.71	0.73
$(311)_{\rm Si}    (1\bar{2}0)_{\beta-{\rm tridymite}}$	8.64/8.61	8.64/9.14	84/90	1.23	1.07	3.12
$(559)_{\rm Si}    (121)_{\alpha-{\rm quartz}}$	7.72/7.39	9.46/10.20	90/84	1.27	0.46	1.05
$(331)_{\rm Si} \  (010)_{\alpha-{\rm crist}}$	8.64/8.49	7.72/8.49	77/72	1.40	0.42	3.56

<sup>a</sup>The effective mass is anisotropic [14]; here, they refer to the heaviest one.

the softening mode direction  $N_t$ , which helps to overcome the barrier between minima:

$$V_{\rm mod} = V_{\rm real} + \sum_{n=1}^{\rm NG} v_n$$
  
=  $V_{\rm real} + \sum_{n=1}^{\rm NG} w_n \times \exp\{-[(R_t - R_t^n) \cdot N_t^n]^2 / (2 \times ds^2)\}.$   
(2)

In Eq. (2), the subscript or superscript *n* is the index of the sequentially added bias potential, and  $R_t$  is the coordination of current structure;  $V_{real}$  is the unmodified PES;  $R_t^n$  is the coordination of reference when  $v_n$  is added. To allow the variation of atom numbers (e.g., different terminations) at the interface, the SSW-NN simulation is coupled with a grand canonical Monte Carlo (GCMC) move every 500 SSW-NN steps, where an O/Si atom can be randomly added or removed. More information on SSW and GCMC method can be found in Supplemental Material [8] and our previous work [13].

By using the above ML-interface method, we are able to search for nonstoichiometric Si/SiO<sub>2</sub> interface structures. Three stable SiO<sub>2</sub> forms, namely,  $\alpha$ -quartz,  $\beta$ -tridymite, and  $\alpha$ -cristobalite ( $\alpha$ -crist) were considered in this Letter. We identified 2497 lattice-matched interfaces from steps 1 and 2, which has a relatively small interfacial area in periodicity that can retain high stability in a short channel. For each interface, we then carried out more than 10<sup>4</sup> minima search using the SSW-NN method to find the most stable structure in step 3.

In 2497 lattice-matched interfaces, there are only 40 interfaces that have an interfacial area with periodicity less than 1 nm<sup>2</sup> [we emphasize that all possible orientations with short periodicity have been explored; see Sec. 1.1(a) of Supplemental Material [8] for more details]. By further excluding the less stable interfaces on the same Si orientation, we finally obtained ten Si/SiO<sub>2</sub> interfaces, as

shown in Fig. 2(b), which should, thus, represent the physical limit for the size of the Si channel. The key properties of these interfaces, including the interfacial energy and the carrier effective mass, are listed in Table I. Note that the listed data for the effective mass corresponds to the heaviest one that determines the carrier transport [14] (also see Supplemental Material [8] for more information).

Among the ten interfaces, the Si(100) interface has the lowest interfacial energy of 0.93  $J/m^2$ , and the high Miller index Si(331) interface has the highest interfacial energy of 1.40  $J/m^2$ . In contrary to intuition, the interfaces of some high-index Si facets, such as Si(531), can have low interfacial energy, being comparable to Si(111) and (110) interfaces that are already used in industry. It should be noticed that our ML-interface method has sampled both crystalline and amorphous structures equivalently, and it turns out that the crystalline interface is always the most stable one (see PES contour plot in Fig. S2 in Supplemental Material [8]). Our identified Si/SiO<sub>2</sub> interface is indeed more stable than interfacial structures proposed previously, including crystalline and amorphous ones (see Table S3 in Supplemental Material [8]), which thus supports strongly the x-ray scattering observation on the ordered  $SiO_2$  at the interface [7].

For the carrier effective mass, the low-index Si facet Si(100) has the lowest electron effective mass  $(m_e^* = 0.23m_e)$ , while Si(110) has the lowest hole effective mass  $(m_h^* = 0.32m_e)$ . Both agree with the established knowledge from experiment [3]. More importantly, our results can now predict the properties of high Miller index surfaces that are still unknown from experiment: Si(331), Si (559), and (210) have low hole effective mass  $\sim 0.5m_e$ , while Si(211) has a low  $m_e^*$  with  $0.63m_e$ . They are, thus, also good candidates for FET interfaces.

Figures 3(a) and 3(b) show the dependence of the tunneling probability on the effective mass and the channel length through a 0.5-eV height potential barrier (a typical barrier height driven by gate voltage in FETs [6]).



FIG. 3. (a),(b) Transmission coefficient vs incident energy for carrier tunneling through a 0.5-eV height potential barrier; (c) partial charge densities at CBM or VBM of  $(100)_{Si} || (111)_{a-crist}$ ,  $(111)_{Si} || (110)_{a-crist}$ , and  $(331)_{Si} || (010)_{a-crist}$  interfaces.

The tunneling probability is described by the transmission coefficients T in the transfer-matrix approach, which approximates an arbitrary potential profile by a stepwise one characterized by potential energy  $U_i$  in each terrace region  $L_i$ :

$$T = \frac{m_1^* k_N}{m_N^* k_1} \left(\frac{1}{M_{11}}\right)^2,$$
 (3)

$$M = \prod_{i=1}^{i=N} \begin{bmatrix} \frac{1}{2} \left( 1 + \frac{m_i^*}{m_{i+1}^i} \frac{k_{i+1}}{k_i} \right) e^{-ik_i L_i} & \frac{1}{2} \left( 1 - \frac{m_i^*}{m_{i+1}^i} \frac{k_{i+1}}{k_i} \right) e^{-ik_i L_i} \\ \frac{1}{2} \left( 1 - \frac{m_i^*}{m_{i+1}^*} \frac{k_{i+1}}{k_i} \right) e^{ik_i L_i} & \frac{1}{2} \left( 1 + \frac{m_i^*}{m_{i+1}^*} \frac{k_{i+1}}{k_i} \right) e^{ik_i L_i} \end{bmatrix},$$
(4)

where  $M_{11}$  is the element of M;  $k_i = \sqrt{2m_i^*(E - U_i)}/\hbar$  the wave vector (also see Supplemental Material [8] for more information). Figure 3(a) shows the transmission coefficients of carriers with  $m^* = 0.23$ , 0.48, and  $0.63m_e$  [corresponding to Si(100), (210), and (211), respectively] with a 3-nm length barrier. The results show that the increase of effective mass from 0.23 to  $0.63m_e$  notably raises the onset incident energy for tunneling from 0.15 to

0.40 eV, indicating a 4-order decrease in tunneling probability according to Fermi-Dirac distribution. Note that the onset incident energy for the  $0.48m_e$  carrier already reaches 0.34 eV, good enough to suppress tunneling. Figure 3(b) further displays the influence of barrier length on tunneling for  $0.48m_e$  carrier, which shows that the 2-nm length barrier maintains an onset incident energy of 0.23 eV, while it diminishes at 1 nm. Therefore, we conclude that the highindex Si/SiO<sub>2</sub> interfaces with  $m^* \sim 0.5m_e$  can efficiently suppress the tunneling down to 2 nm scale.

we Below, select  $(100)_{\rm Si} \| (111)_{\alpha-{\rm crist}} \|$ and  $(331)_{\rm Si} || (010)_{\alpha-{\rm crist}}$  as the example to illustrate the interface atomic structure with low and high Miller index Si facets. The bare Si(100) surface exposes two-coordinated Si atoms, and, in forming the interface, these surface Si atoms pair with each other to form Si-Si dimers, leading to a  $(2 \times 1)$  reconstruction. This turns off half of the dangling bonds, and the remaining dangling bonds are then passivated by the terminal O atoms from the  $SiO_2$  side. It might be emphasized that our predicted  $(2 \times 1)$  reconstruction is well consistent with the x-ray diffraction pattern of the  $Si(100)/SiO_2$  interface in experiment [15].

For the high interfacial energy interface Si(331)/SiO<sub>2</sub>, the exposed dangling bonds from the Si surface are not fully terminated by the SiO<sub>2</sub> phase. Si(331) consists of alternate (111) and (110) narrow terraces, and each Si atom on the terraces exposes one dangling bond. In forming the interface, there are still two dangling bonds per (8.64 Å × 7.72 Å) unit cell untouched [highlighted by dashed cycles in Fig. 2(b)], which rationalizes the high interfacial energy of Si(331)/SiO<sub>2</sub>.

By examining the ten interface structures [Fig. 2(b)], we find that no interfacial dangling bonds are present on the Si(100), (111), (511), and (531) faces, but Si(110), (559), (210), (211), (311), and (331) interfaces do have a number of dangling bonds. This indicates the dependence of the interfacial-dangling-bond contents on Si orientations, which can be directly measured by H<sub>2</sub> passivation in experiment. Indeed, Ogata et al. reported that the interface state densities on Si(100) and (111) have no change after H<sub>2</sub> passivation, indicating no dangling bonds are present on these two interfaces. In contrast, the interface state densities on Si (110), (210), (311), and (331) remarkably decrease by up to 1 order after the same treatment [16]. The experimental observations are consistent with our theoretical prediction. Since the dangling bonds are the source of interface states that trap the carrier and reduce the performance of FETs, theoretical predictions are of direct significance for the design of Si/SiO<sub>2</sub> interface for FETs.

Even after passivating the interfaces by H, the interface states can still persist, which can also be predicted from theory. By examining the wave functions of all ten interfaces after H passivation, as illustrated in the contour plots of conduction band minimum (CBM) and valence band maximum (VBM) (also see Fig. S3 in Supplemental Material [8] for more information), we found that the interfaces of Si(100), (110), (210), (211), (511), (559), and (331) are ideal interfaces with no carrier trapping sites. Furthermore, Si(531) and (311) have no hole trapping site but can trap electrons at the interface due to the presence of the localized CBM. On the contrary, Si(111) has no electron trapping site but can trap holes due to the presence of localized VBM at the interface [see Fig. 3(c)].

Our data on  $Si/SiO_2$  interfaces shed new light on the design of the next-generation FETs. To date, Si(100) and (110) are still the facets utilized in *n*-type and *p*-type devices in FinFET, respectively. However, because of their low carrier effective mass (~ $0.2m_e$ ), the off-state leakage current has limited the further decrease of the channel length. For instance, the channel length in Intel's newest 10-nm technology node is 18 nm, which is only 2 nm shorter than that in the 14-nm technology node announced in 2014. To surmount this limit, we propose that high-index Si(211) and (210) could be promising candidates for future short-channel FETs. There are several merits for these two interfaces. First, the carrier effective masses at both interfaces  $[m_e^* = 0.63m_e$  for Si(211) and  $m_h^* = 0.48m_e$ for Si(210)] are close to that of  $MoS_2$  (0.55m<sub>e</sub>), which can suppress the off-state leakage while maintaining the onstate current. Second, the wave functions of CBM and VBM are well delocalized within the Si phase, which restrains carrier trapping at the interface. Third, both Si(210) and (211) can form stable interfaces with  $SiO_2$ , with small interfacial energies of 1.16 and  $1.17 \text{ J/m}^2$ , respectively. Fourth, the size of both Si/SiO<sub>2</sub> interfaces is less than 1.2 nm in periodicity, which allows the downcaling of the channel length to  $\sim 2$  nm.

To recap, we develop a ML-interface method that can systematically search for stable interfaces between any two solid phases. We now screen out ten stable Si/SiO<sub>2</sub> interfaces ( $\gamma < 1.4 \text{ J/m}^2$ ) with an interfacial area less than  $1 \text{ nm}^2$  in periodicity. Among them, Si(210) and (211) are promising candidates for the applications in the future short-channel FETs, with good carrier effective mass  $(\sim 0.5 m_{e})$ , low carrier trapping, high interface stability, and short periodicity. Our results suggest that, in addition to the low-index interfaces, the high-index Si/SiO<sub>2</sub> interfaces do also deliver the desired thermodynamics and transport properties. With the precise control in lattice orientation, Si should still be possible to act as the key element for FinFET down to 1 nm. Our ML-interface method with only the bulk structure of material as input can be generally applied to predict the interfaces in future FETs with emerging semiconductor materials, such as SiC and GaN.

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